

FIG. 1
(PRIOR ART)

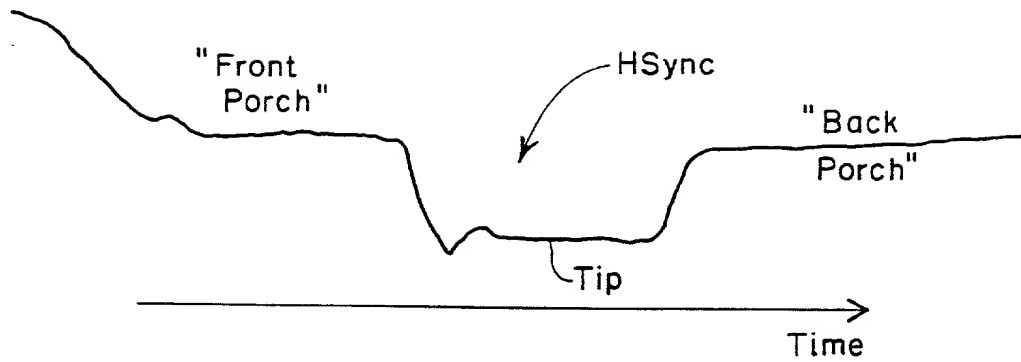


FIG. 1A

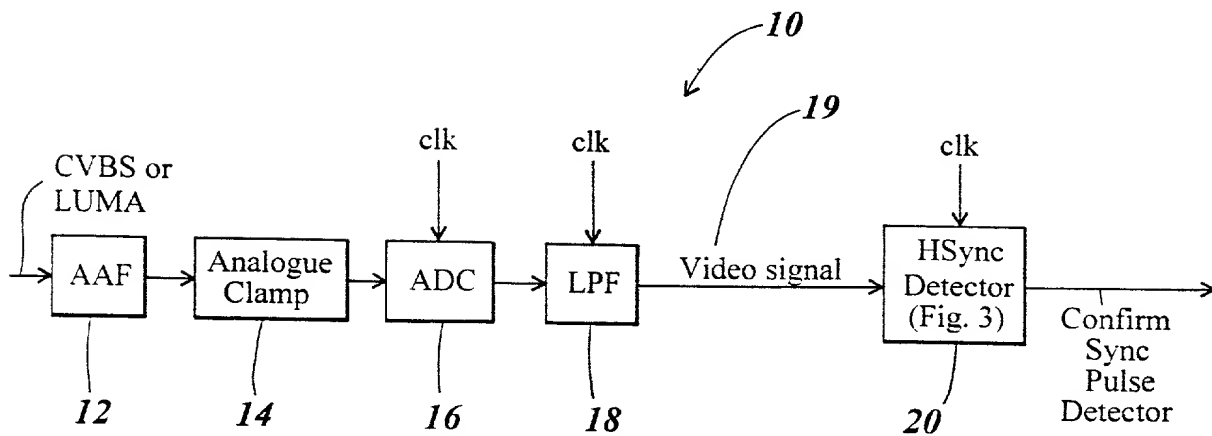
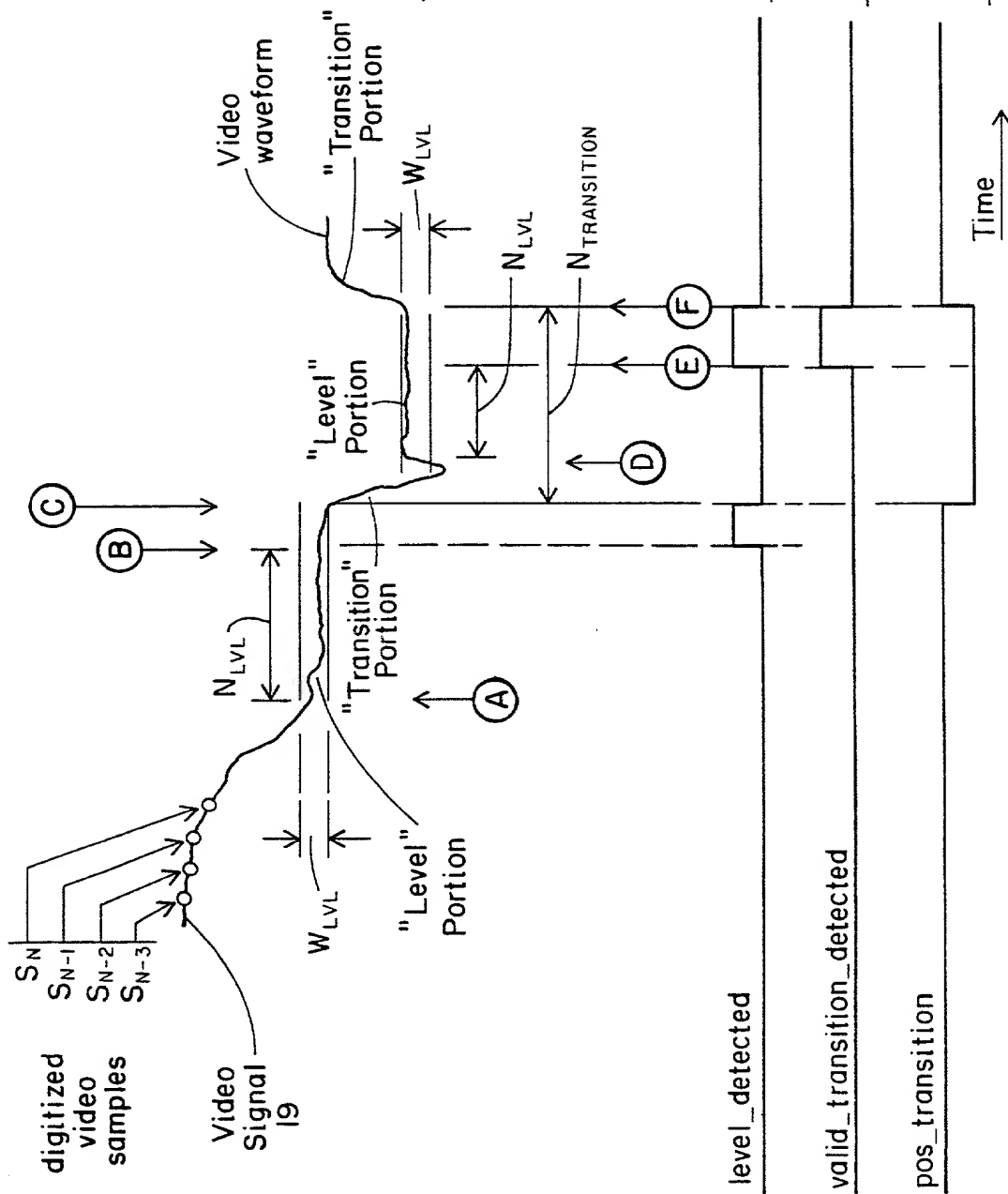


FIG. 2



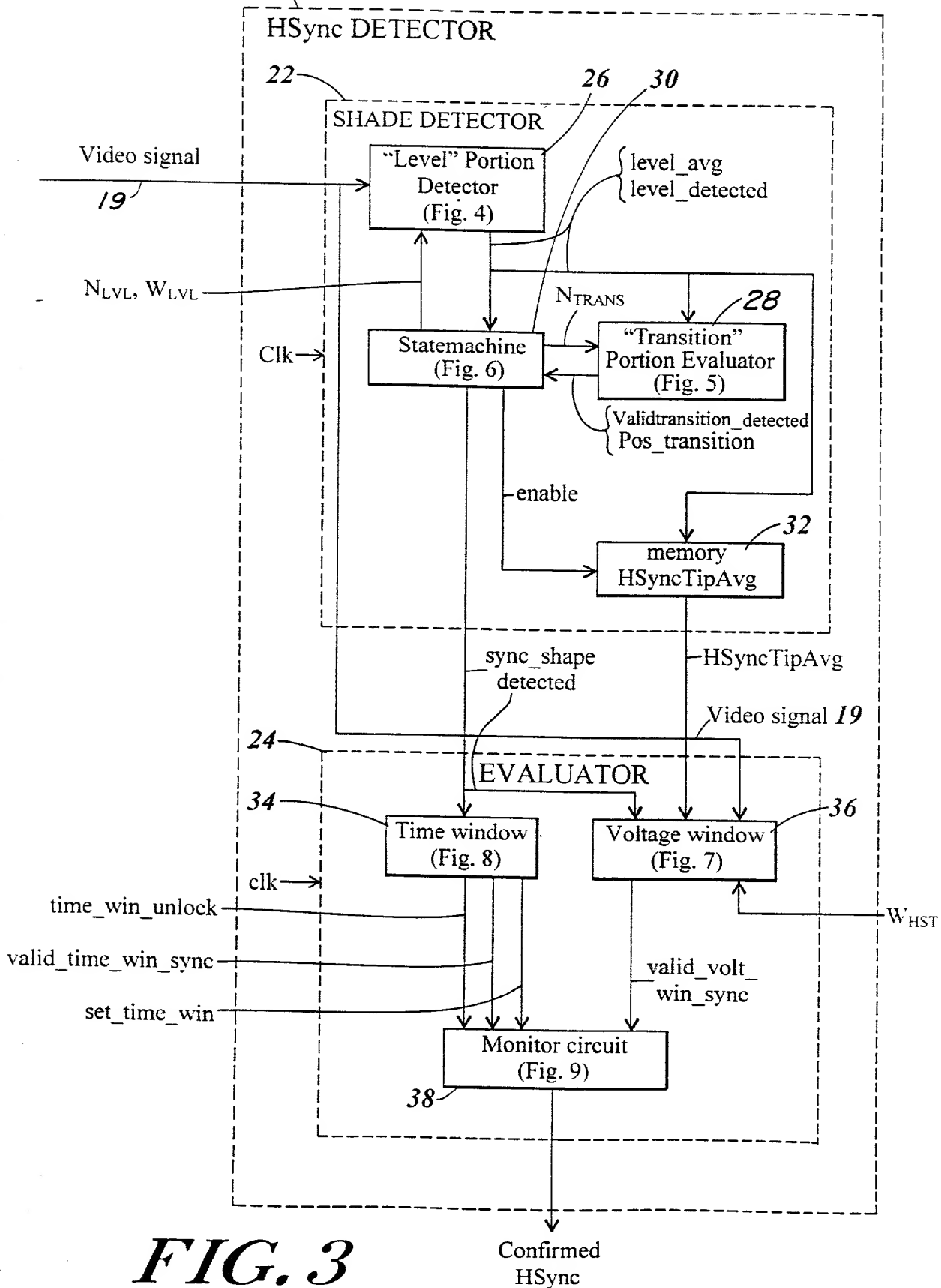


FIG. 3

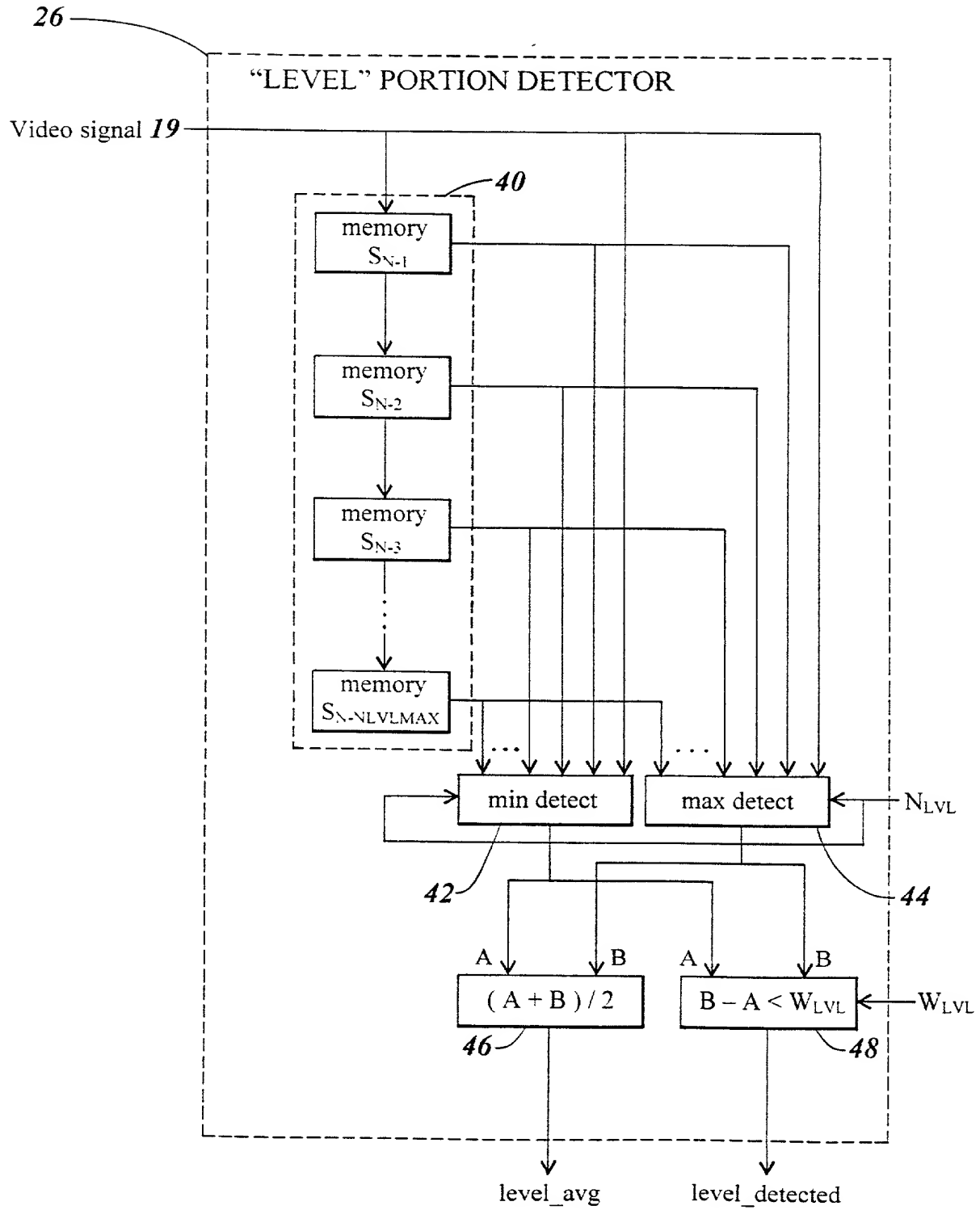


FIG. 4

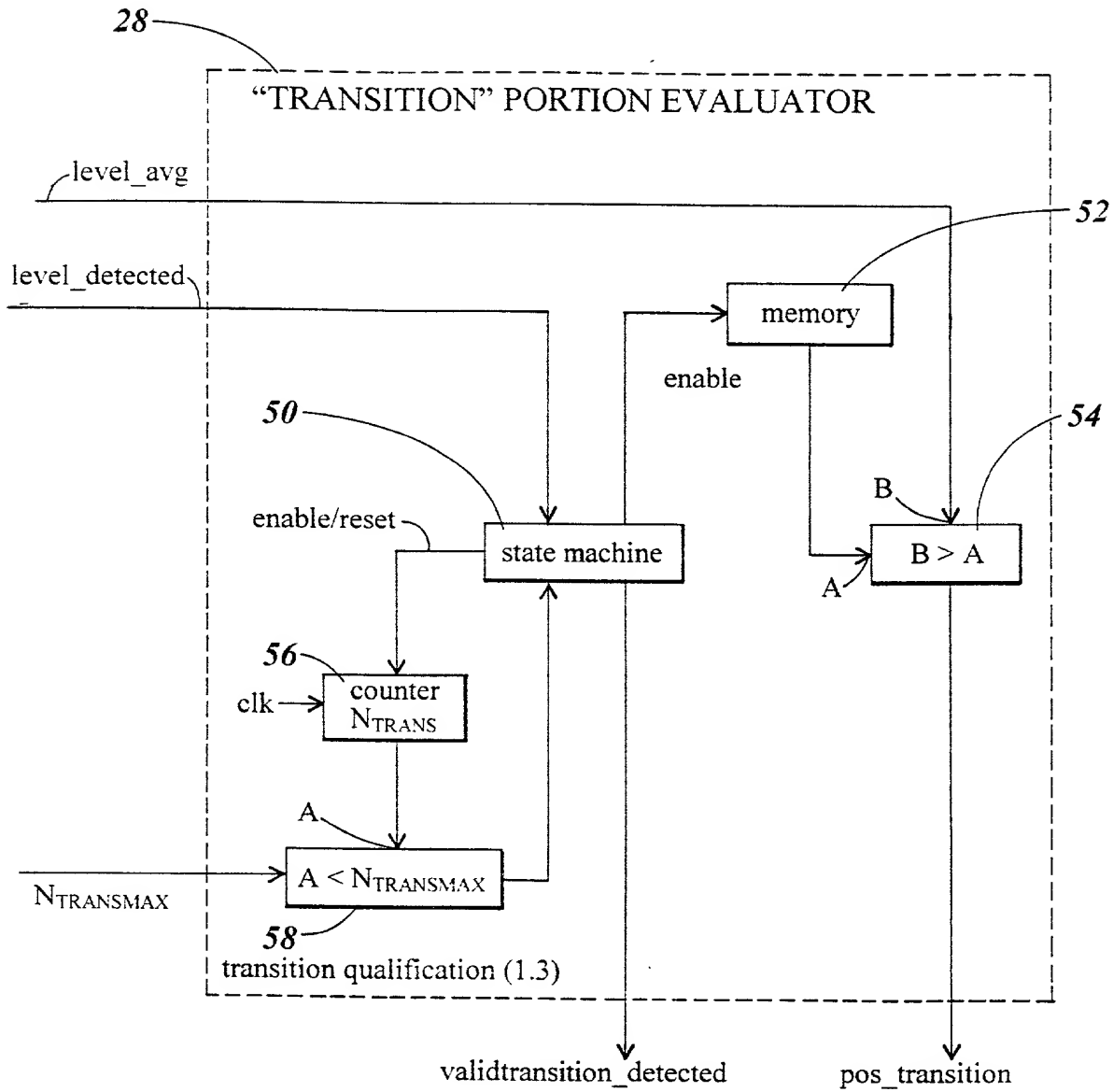


FIG. 5

STATEMACHINE PROGRAM

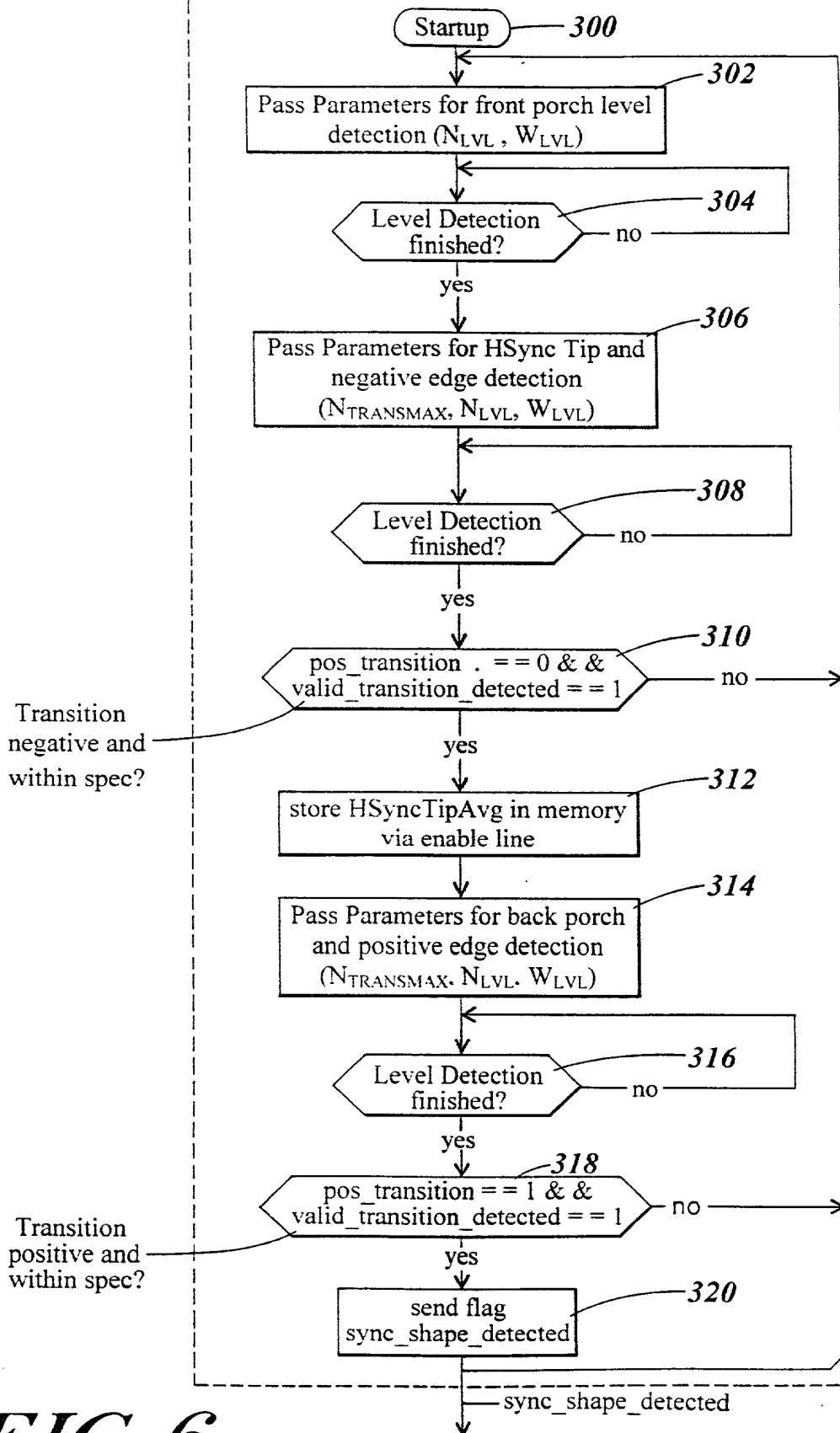


FIG. 6

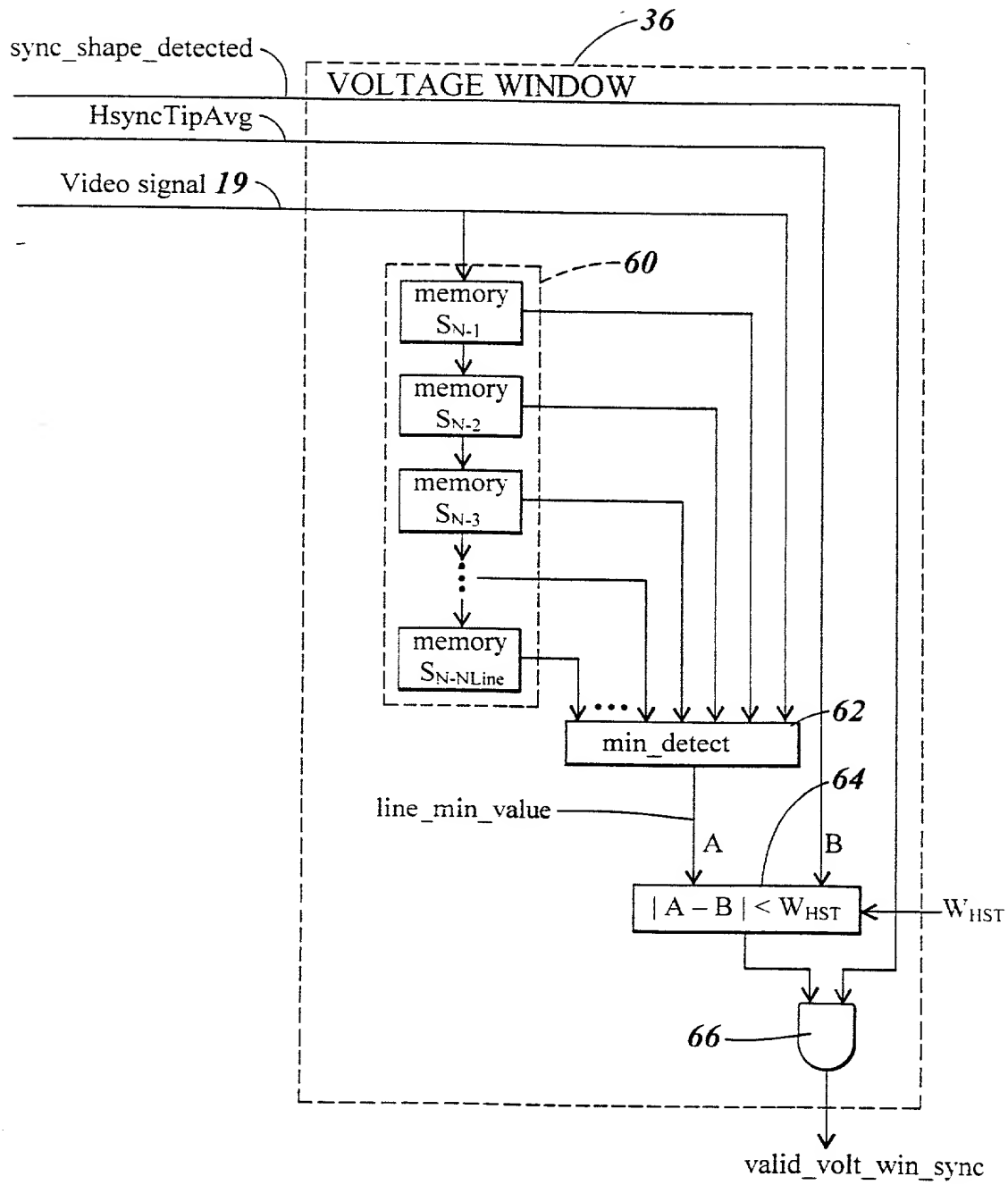


FIG. 7

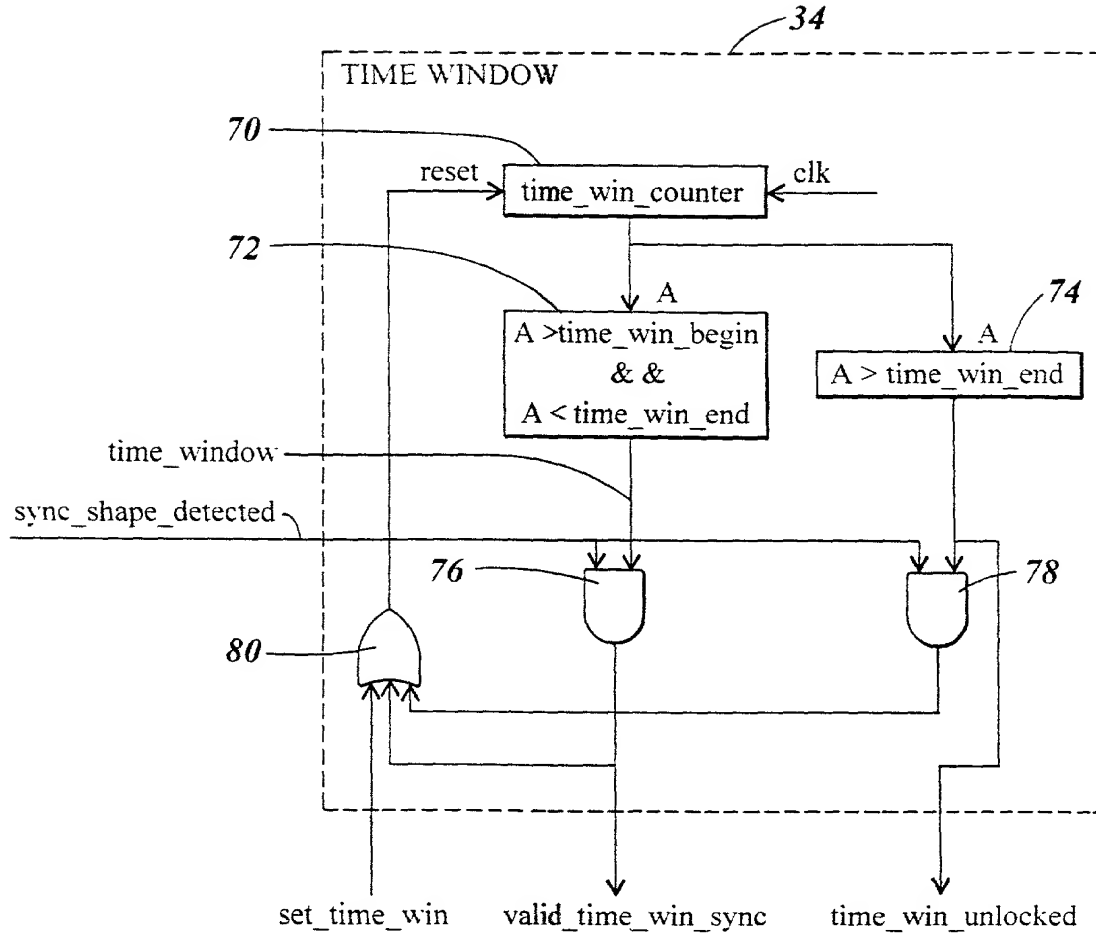


FIG. 8

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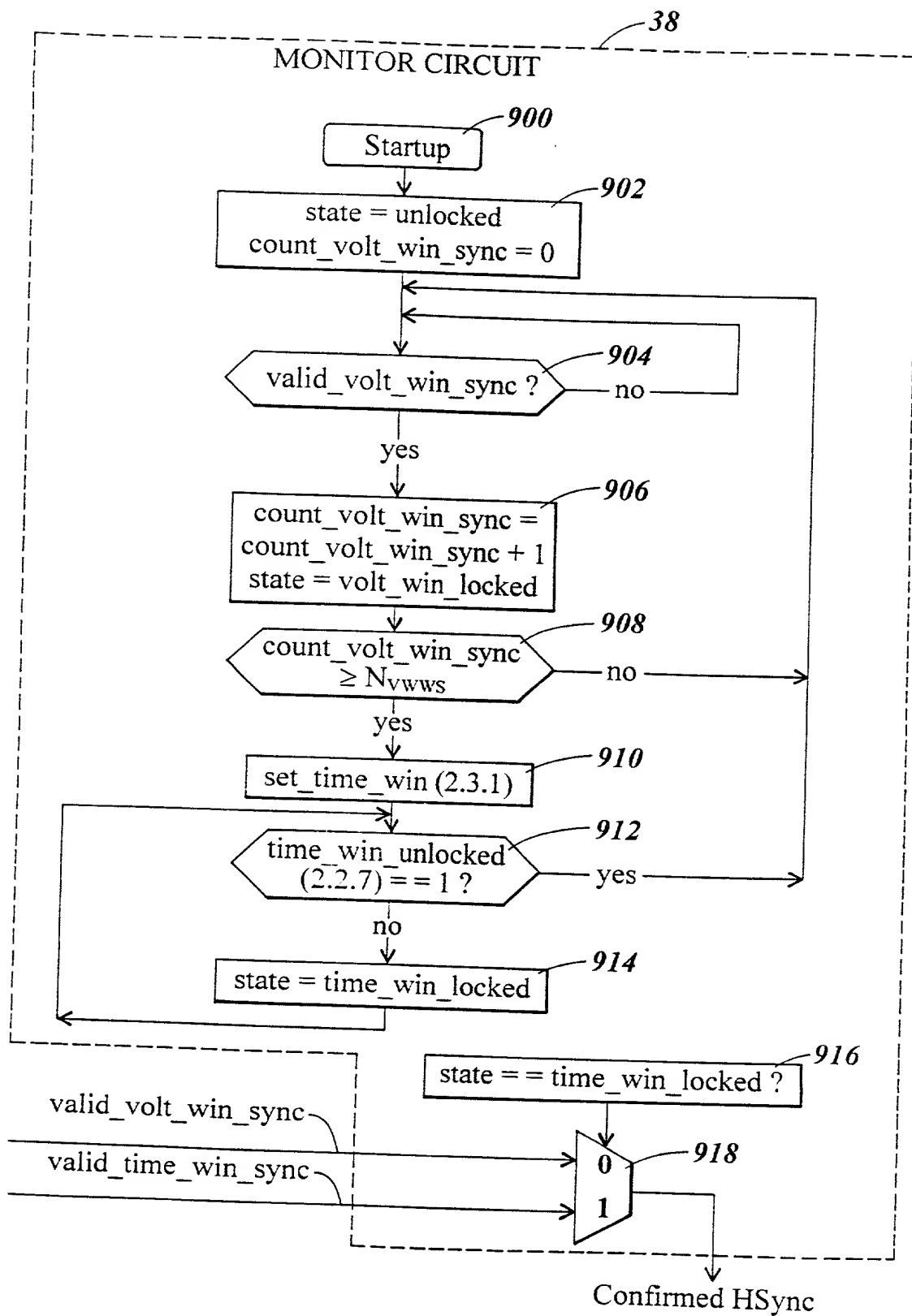


FIG. 9

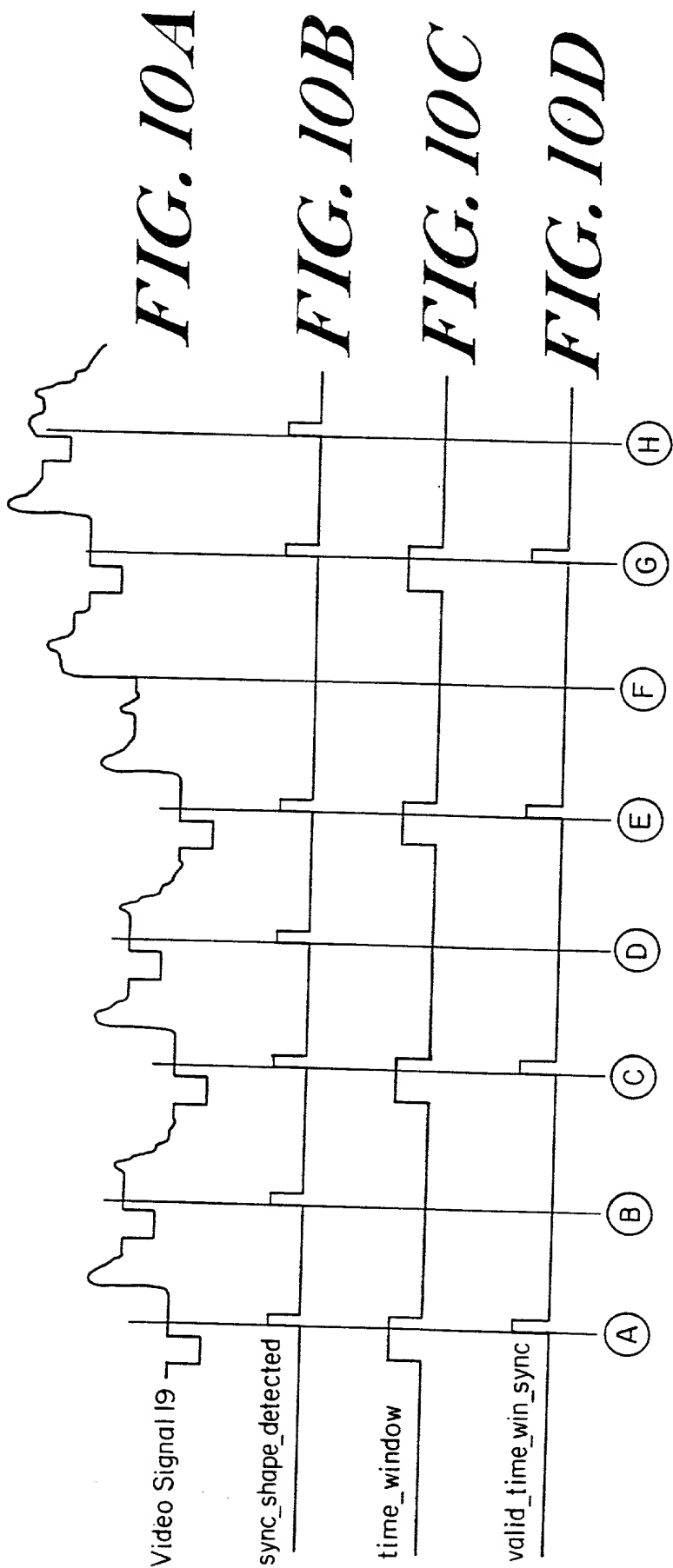


FIG. 10A

FIG. 10B

FIG. 10C

FIG. 10D

